

FIG 3A

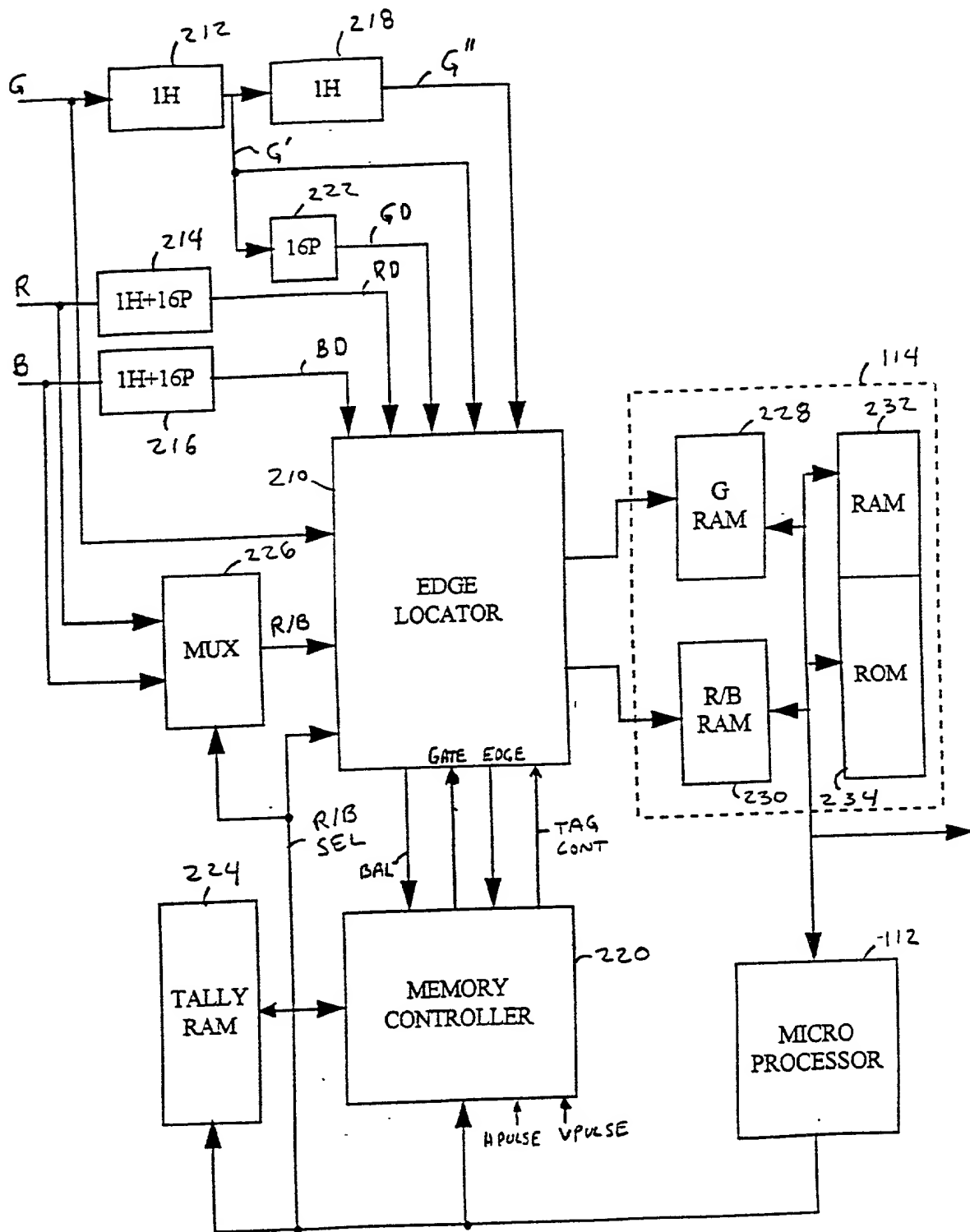


FIG 2

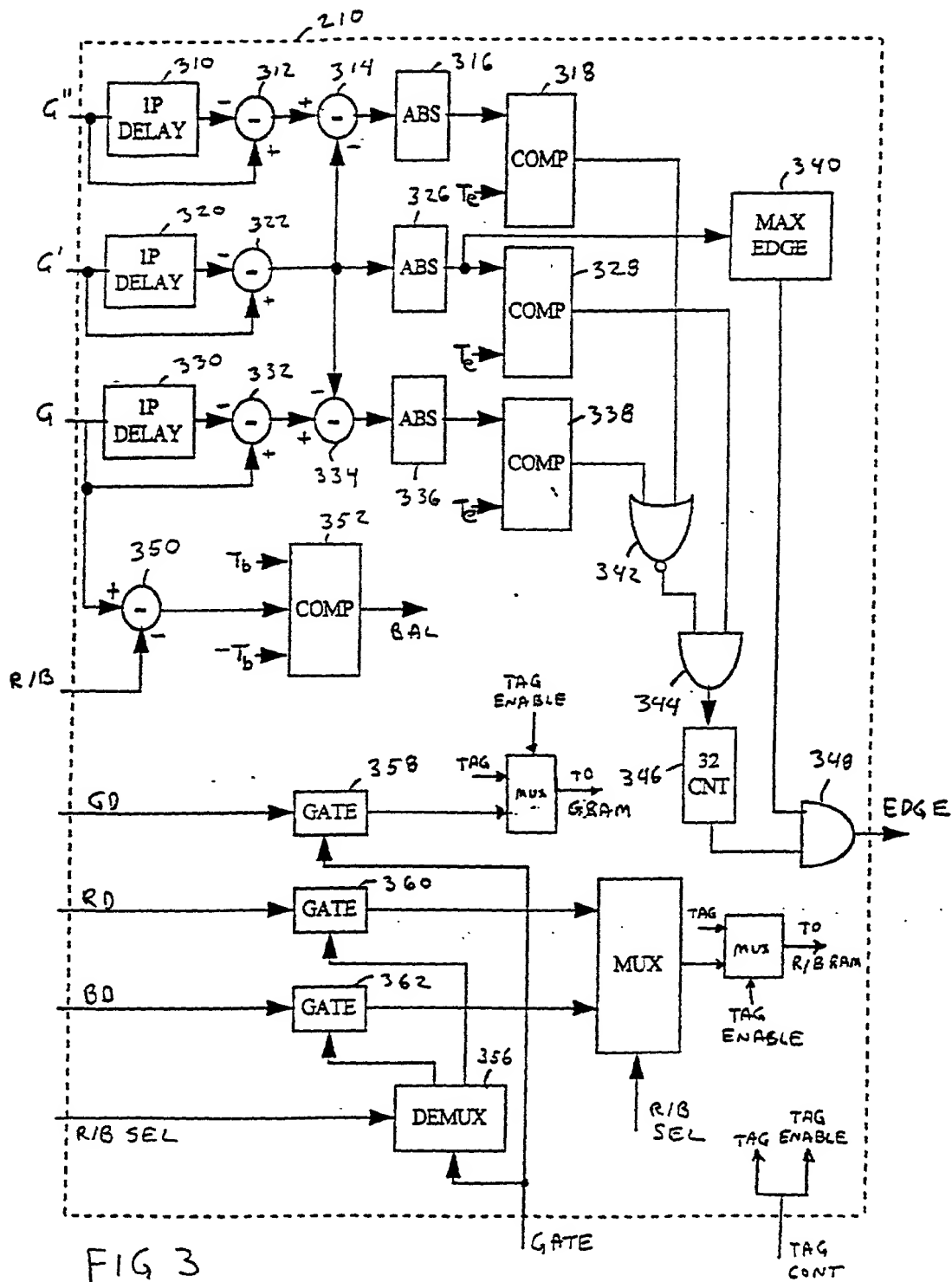


FIG 3

The diagram illustrates the video RAM control logic, featuring several interconnected components:

- 4-bit counter (410):** Receives a clock signal and outputs a 4-bit signal to the 32-pixel count (420).
- 32-pixel count (420):** Outputs a 5-bit VLSB signal.
- 32768 zone counter (418):** Receives a clock signal and outputs a 15-bit signal to the 200 zone counter (428).
- 200 zone counter (428):** Receives a clock signal and outputs a 8-bit signal to the 32-pixel count (420).
- Logic gates (402, 404, 406, 411, 412, 413, 414, 415, 416):** Perform various logical operations on the input signals.
- Multiplexers (MUX 424, MUX 430):** Select between different data paths based on control signals.
- Control signals:** VideoRAM Write Enable, RSEL, VideoRAM Output Enable, and VideoRAM Address.

Fig. 4

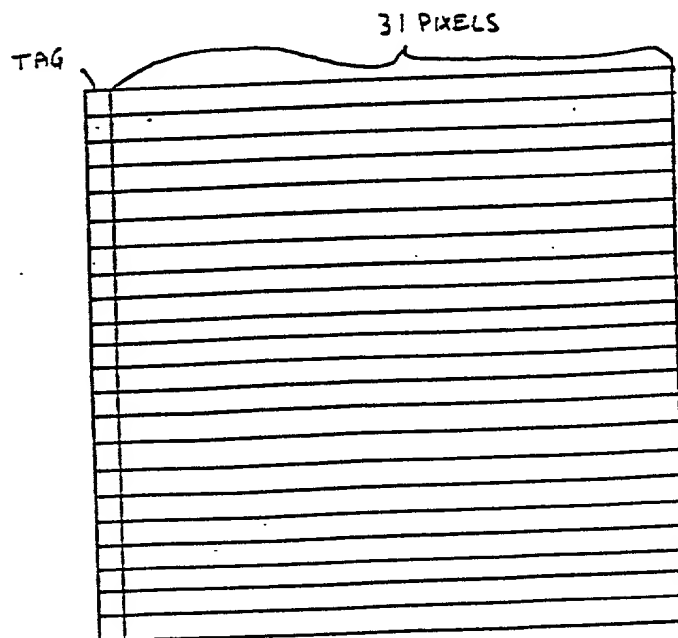
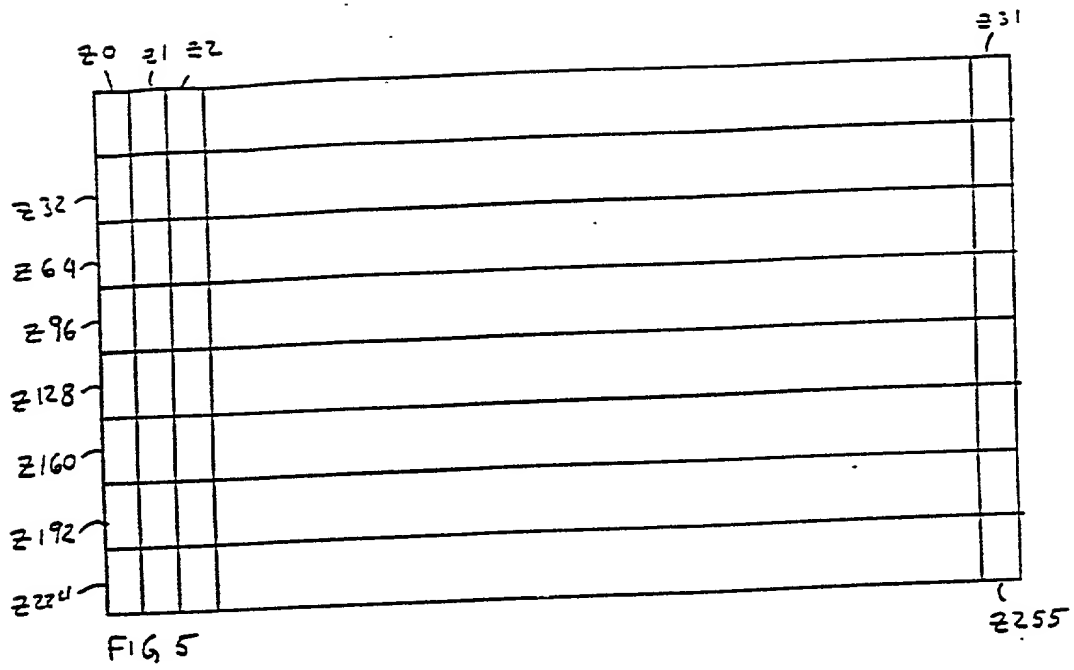


FIG 6

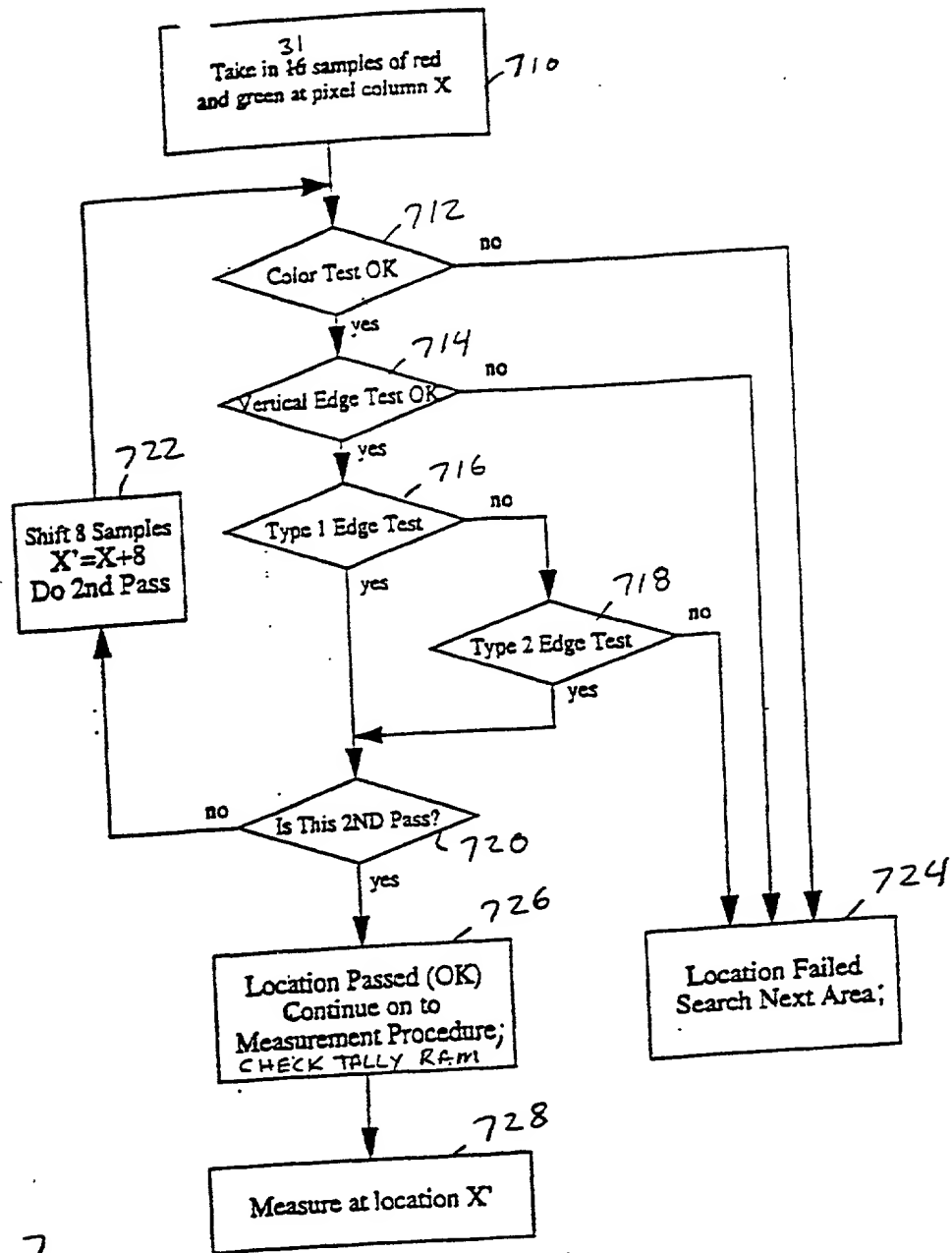


FIG 7

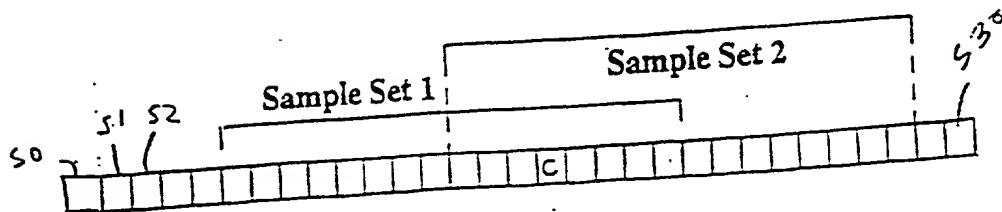


FIG 8